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	Applicati n No.	Applicant(s)	
Notice of Allowability	10/611,898	YOO ET AL.	
	Examiner	Art Unit	
	Asok K. Sarkar	2829	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	ears on the cover sheet with (OR REMAINS) CLOSED in (OR other appropriate community) or other appropriate community (RIGHTS). This application is s	this application. If not includ inication will be mailed in due	ed course. THIS
1. This communication is responsive to Application filed 7/3/	<u>2003</u> .		
2. The allowed claim(s) is/are <u>1-10</u> .			
3. The drawings filed on <u>03 July 2003</u> are accepted by the E	xaminer.		
 4. Acknowledgment is made of a claim for foreign priority units a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONITHIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which given 	e been received in Application occuments have been received of this communication to file MENT of this application.	n No I in this national stage application of No a reply complying with the re	quirements
6. CORRECTED DRAWINGS (as "replacement sheets") mu	ist be submitted.		
(a) ☐ including changes required by the Notice of Draftsper	son's Patent Drawing Review	v (PTO-948) attached	
1) hereto or 2) to Paper No./Mail Date			
(b) including changes required by the attached Examiner Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	1.84(c)) should be written on the header according to 37 CF	ne drawings in the front (not the R 1.121(d).	e back) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	osit of BIOLOGICAL MATE FOR THE DEPOSIT OF BIO	ERIAL must be submitted. DLOGICAL MATERIAL.	Note the
Attachment(s) 1. Notice of References Cited (PTO-892) 2. □ Notice of Draftperson's Patent Drawing Review (PTO-948)		formal Patent Application (PT ummary (PTO-413),	O-152)
 Notice of Draftperson's Patent Drawing Review (PTO-948) Information Disclosure Statements (PTO-1449 or PTO/SB/ 	Paper No.	Mail Date Amendment/Comment	
Paper No./Mail Date 4.	_	Statement of Reasons for All	owance
of Biological Material	9.		

DETAILED ACTION

EXAMINER'S AMENDMENT

- 1. The Applicant's representative was contacted in order to amend the abstract to reduce its size below 150 words and to rewrite the step (b) of claim 1 in order to clearly state the limitations of the step.
- 2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Benjamin Hauptman on June 9, 2004.

The application has been amended as follows:

In the Abstract, in line 2, following the word 'diodes', delete "is provided. The method comprises the steps of, " and insert - - starts by - - .

Replace claim 1 with the attached copy of the new version of claim 1.

Allowable Subject Matter

- 3. Claims 1 10 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

Claims 1 – 10 recite, inter alia, a method of manufacturing GaN light emitting diodes comprising the steps of partially cutting the light emitting structure formed on a substrate into plural units so that at least approximately 100 Å of the thickness of the first conductive GaN clad layer remains on the substrate, attaching a conductive

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substrate to exposed upper surfaces of the unit light emitting structures using a conductive adhesive layer, irradiating a laser beam on the sapphire substrate to remove the sapphire substrate from the unit light emitting structures, wherein the residual first conductive GaN clad layer is removed so that the light emitting structure can be perfectly divided into the unit light emitting structures with a size same as that of light emitting diodes to be finally manufactured, forming contacts and cutting the resulting structure along the cut lines of the unit light emitting structures into plural unit light emitting diodes. Although, Kelly, US 6,559,075 teaches a method of separating the substrate from a GaN LED device by illuminating with an electromagnetic radiation, attaching a conductive substrate and applying contacts on both sides, he fails to teach the step of partially cutting the structure before attaching the second substrate and completely separating the LEDs after applying the contacts on both sides of the devices. Additionally, the art of record does not disclose or anticipate the above limitation in combination with other claim elements nor would it be obvious to modify the art of record so as to form a device including the above limitation.

Conclusion

The prior art made of record and not relied upon is considered pertinent to 5. applicant's disclosure.

Shakuda, US 5,557,115; Yamamoto, US 5,994,205; Haerie, US 6,100,104; Ishida, US 6,562,701; Liu, US 6,665,820 and Ogawa, US 22002/0182839 teach methods for fabricating GaN LEDs.

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6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Asok K. Sarkar whose telephone number is 571 272 1970. The examiner can normally be reached on Monday - Friday (8 AM- 5 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571 272 1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

8. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Asok K. Sarkar June 10, 2004

Patent Examiner

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

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